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11000 U.S. PTO
09/974806
10/12/01

Subclass	ISSUE CLASSIFICATION
Class	

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PATENT NUMBER

U.S. UTILITY Patent Application

JC588

O.I.P.E.	PATENT DATE
SCANNED EM NS1 Q.A. GW	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/974806	D F	345	211	2674	DINH, Duc

APPLICANTS

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TITLE

Flat display panel having internal lower supply circuit for reducing power consumption

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION			
			☐ Continued on Issue Slip Inside File Jacket

☒ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
☒ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. 6,522,314				ISSUE FEE	
				Amount Due	Date Paid
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(Assistant Examiner) (Date)

(Primary Examiner) (Date)

(Legal Instruments Examiner) (Date)

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